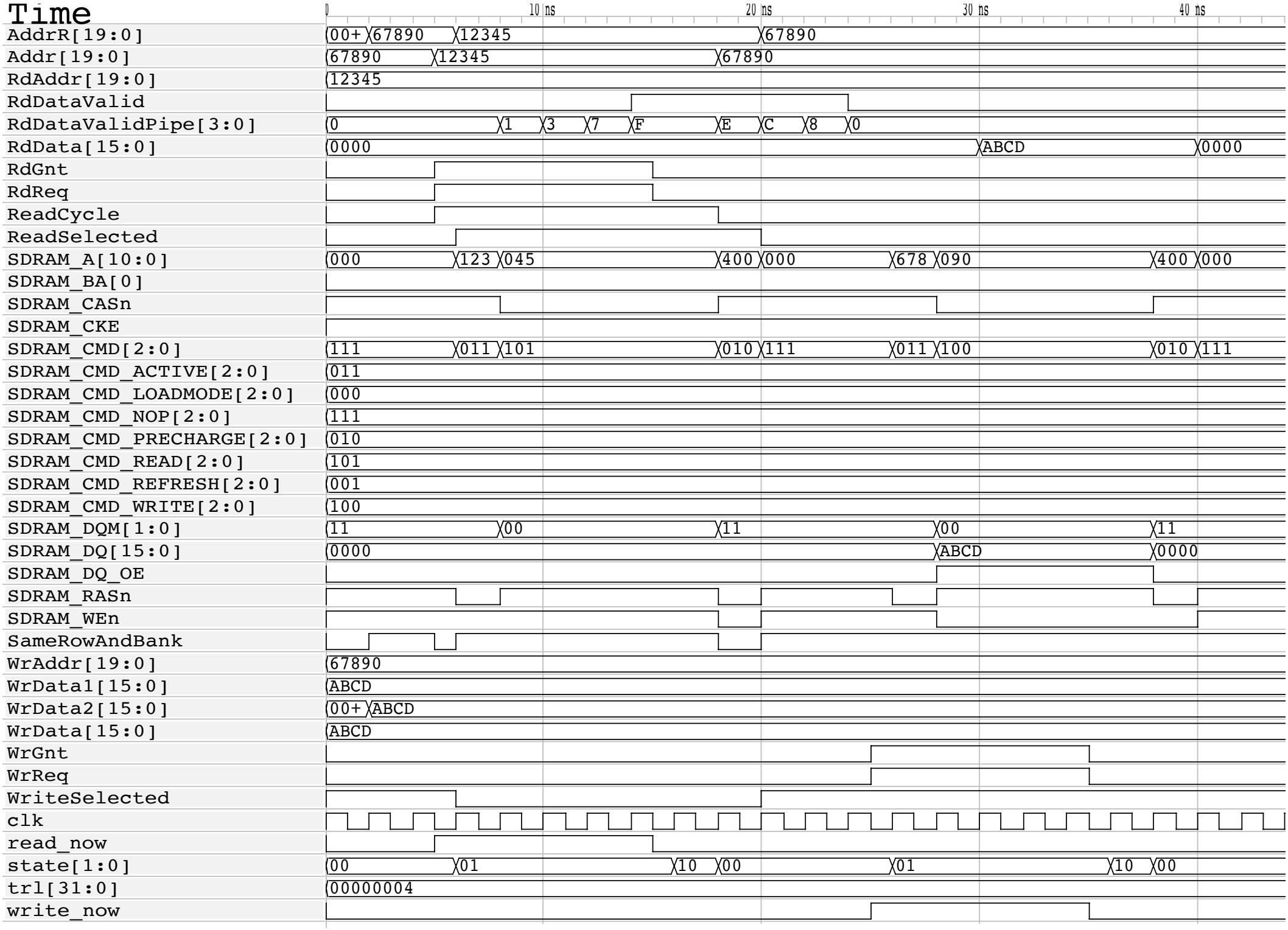
Time
0
10 ns
20 ns
30 ns
40 ns
AddrR[19:0]
00+
67890
12345
67890
Addr[19:0]
67890
12345
67890
RdAddr[19:0]
12345
RdDataValid
RdDataValidPipe[3:0]
0
1
3
7
F
E
C
8
0
RdData[15:0]
0000
ABCD
0000
RdGnt
RdReq
ReadCycle
ReadSelected
SDRAM_A[10:0]
000
123
045
400
000
678
090
400
000
SDRAM_BA[0]
SDRAM_CASn
SDRAM_CKE
SDRAM_CMD[2:0]
111
011
101
010
111
011
100
010
111
SDRAM_CMD_ACTIVE[2:0]
011
SDRAM_CMD_LOADMODE[2:0]
000
SDRAM_CMD_NOP[2:0]
111
SDRAM_CMD_PRECHARGE[2:0]
010
SDRAM_CMD_READ[2:0]
101
SDRAM_CMD_REFRESH[2:0]
001
SDRAM_CMD_WRITE[2:0]
100
SDRAM_DQM[1:0]
11
00
11
00
11
SDRAM_DQ[15:0]
0000
ABCD
0000
SDRAM_DQ_OE
SDRAM_RASn
SDRAM_WEn
SameRowAndBank
WrAddr[19:0]
67890
WrData1[15:0]
ABCD
WrData2[15:0]
00+
ABCD
WrData[15:0]
ABCD
WrGnt
WrReq
WriteSelected
clk
read_now
state[1:0]
00
01
10
00
01
10
00
trl[31:0]
00000004
write_now